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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/550,343	09/23/2005	Mihai Adrian Tiberiu Sanduleanu	NL 030306	1329
2.15.	7590 03/23/200 LLECTUAL PROPER	EXAMINER		
P.O. BOX 3001		CHANG, JOSEPH		
BRIARCLIFF MANOR, NY 10510			ART UNIT	PAPER NUMBER
		2817		
SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
3 MONTHS 03/23/2007		PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

		Application No.	Applicant(s)
Office Action Summary		10/550,343	SANDULEANU ET AL.
		Examiner .	Art Unit
		Joseph Chang	2817
Period fo	The MAILING DATE of this communication or Reply	n appears on the cover sheet with	the correspondence address
WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR RICHEVER IS LONGER, FROM THE MAILIN asions of time may be available under the provisions of 37 Cf SIX (6) MONTHS from the mailing date of this communication period for reply is specified above, the maximum statutory pre to reply within the set or extended period for reply will, by seply received by the Office later than three months after the end patent term adjustment. See 37 CFR 1.704(b).	G DATE OF THIS COMMUNICA FR 1.136(a). In no event, however, may a reply n. eriod will apply and will expire SIX (6) MONTHS statute, cause the application to become ABANI	TION. be timely filed From the mailing date of this communication. DONED (35 U.S.C. § 133).
Status		4	
2a)□	Responsive to communication(s) filed on 2 This action is FINAL . 2b) Since this application is in condition for all closed in accordance with the practice unc	This action is non-final. owance except for formal matters	•
Dispositi	on of Claims		
5) □ 6) ⋈ 7) ⋈ 8) □ Applicati 9) □ 10) ⋈	Claim(s) 1-9 is/are pending in the applicat 4a) Of the above claim(s) is/are with Claim(s) is/are allowed. Claim(s) 1,2 and 6 is/are rejected. Claim(s) 3-5 and 7-9 is/are objected to. Claim(s) are subject to restriction a on Papers The specification is objected to by the Example drawing(s) filed on 23 September 200.	ndrawn from consideration. nd/or election requirement. miner. 5 is/are: a) □ accepted or b) □ o	
_	Applicant may not request that any objection to Replacement drawing sheet(s) including the co The oath or declaration is objected to by the	prrection is required if the drawing(s)	is objected to. See 37 CFR 1.121(d).
Priority u	inder 35 U.S.C. § 119		
12)⊠ <i>a</i>)[Acknowledgment is made of a claim for for All b) Some * c) None of: 1. Certified copies of the priority docur 2. Certified copies of the priority docur 3. Copies of the certified copies of the application from the International Butter the attached detailed Office action for a	ments have been received. ments have been received in Appl priority documents have been rec ureau (PCT Rule 17.2(a)).	lication No ceived in this National Stage
2) Notic 3) Inform	t(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-946 nation Disclosure Statement(s) (PTO/SB/08) r No(s)/Mail Date <u>9/23/05</u> .	B) Paper No(s)/M	nmary (PTO-413) fail Date mal Patent Application

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Matsumoto et al., US Patent 5,625,308.

Matsumoto et al. discloses in Fig. 10 a track and hold circuit comprising: a linear amplifier (3,4,5,6,9) receiving a differential analog signal (Vin1, Vin2) and being controlled by a first binary clock signal (x) having a first phase and a second phase, the linear amplifier (3,4,5,6,9) providing a feed-forward input signal substantially equal with the differential analog signal (Vin1, Vin2); a pseudo latch circuit (7,8,10) being controlled by a second binary clock signal (x bar) for memorizing the feed forward input signal and for providing a differential output signal (Vout1, Vout2) substantially equal with the feed-forward input signal during the second phase of the first binary clock signal (x bar), the second binary clock signal being substantially in anti-phase with the first binary clock signal (x).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and

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the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Matsumoto et al. in view of Shkap, US Patent 6,404,285.

As noted above, Matsumoto et al. discloses a track and hold circuit including a linear amplifier except the feature recited in claim 2 that is common drain transistor. As would have been well known in the art such feature is used for optimum power transfer (col.2, lines 55-56).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to use such feature because such a modification would have provided the benefit stated above.

Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Matsumoto et al. in view of Suzuki, US Patent 5,081,373.

As noted above, Matsumoto et al. discloses a track and hold circuit including a linear amplifier except the feature recited in claim 3 that is an output signal having a half rate. As would have been well known in the art such feature is used for optimum performance (col.4, lines 8-19).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to use such feature because such a modification would have provided the benefit stated above.

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Allowable Subject Matter

Claims 3-5, and 7-9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter: the best prior art of record, Matsumoto et al, taken alone or in combination of other references, does not teach or fairly suggest the specific feature as set forth in the claims.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Lane discloses a feedforward differential amplifier.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph Chang whose telephone number is 571 272-1759. The examiner can normally be reached on Mon-Fri 0700-1730.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JOSEPH CHANG PRIMARY EXAMINER

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